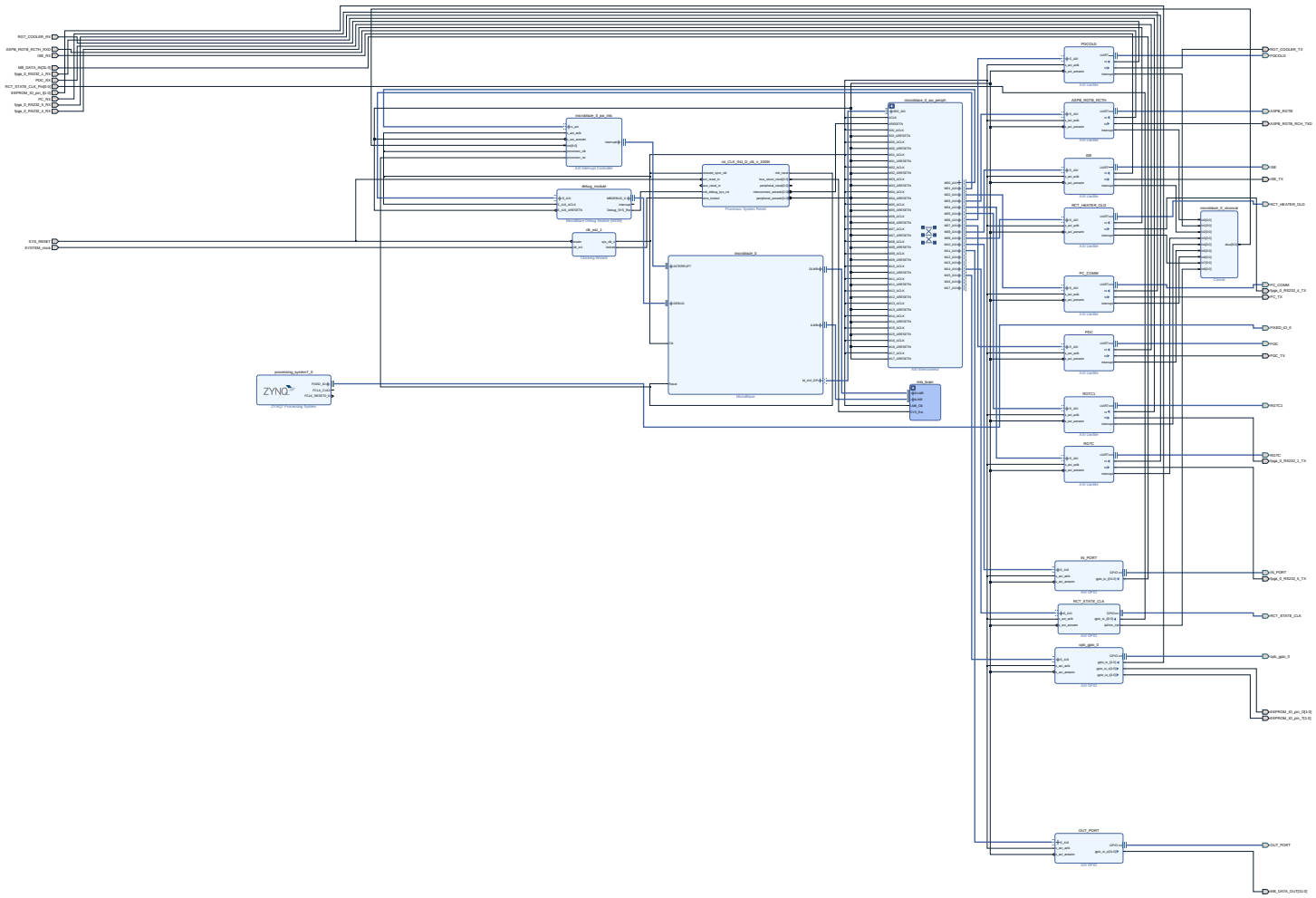

RST_COOLER_RX
ASPB_ROTB_RCTH_RXD
IBB_RX
MB_DATA_IN[0:0]
fpga_0_RS232_1_RX
PDC_RX
RCT_STATE_CLK_PH[0:0]
EEPROM_IO_pin_I[1:0]
PC_RX
fpga_0_RS232_3_RX
fpga_0_RS232_4_RX
SYS_RESET
SYSTEM_clock
processing_system7_0
ZYNQ
PS7
PS7_Processing System
microblaze_0_axi_intc
AXI Interrupt Controller
debug_module
MBDEBUG_0
Debug_SYS_Rx
MB Debug Module Module (MDM)
clk_wiz_1
Clocking Wizard
rst_CLK_RX_0_clk_x_100M
Processor System Reset
microblaze_0
Microblaze
microblaze_0_axi_periph
AXI Interconnect
MB_Bram
PDCOLD
ASPB_ROTB_RCTH
IBB
RCT_HEATER_OLD
PC_COMM
PDC
RSTC1
RSTC
IN_PORT
RCT_STATE_CLK
rsb_gpm_0
OUT_PORT
AXI Uartlite
AXI Gpio
microblaze_0_dbus_ai
Vector
RST_COOLER_TX
PDCOLD
ASPB_ROTB
ASPB_ROTB_RCTH_TXD
IBB
IBB_TX
RCT_HEATER_OLD
PC_COMM
fpga_0_RS232_4_TX
PC_TX
PSXED_IO_0
PDC
PDC_TX
RSTC1
RSTC
fpga_0_RS232_1_TX
IN_PORT
fpga_0_RS232_3_TX
RCT_STATE_CLK
rsb_gpm_0
EEPROM_IO_pin_I[1:0]
EEPROM_IO_pin_T[1:0]
OUT_PORT
MB_DATA_OUT[1:0]